L	Hits	Search Text	DB	Time stamp
Number	HITS	Search Text	DB	Time Stamp
6	86	((((dma and ram and (address adj	USPAT	2003/12/09
		count\$5) and interrupts) and		16:58
		register\$1) and (start\$6 adj address))		1
		and predetermined) and temporary) and		
		(signal adj process\$7)and arithmetic		
7	83	(((((dma and ram and (address adj	USPAT	2003/12/09
		count\$5) and interrupts) and		16:48
		register\$1) and (start\$6 adj address))		1
		and predetermined) and temporary) and		
		(signal adj process\$7) and arithmetic)		
8		and logic		
	83	((((((dma and ram and (address adj	USPAT	2003/12/09
		count\$5) and interrupts) and	İ	16:48
		register\$1) and (start\$6 adj address))		
		and predetermined) and temporary) and		
		(signal adj process\$7) and arithmetic)		
	1.4	and logic) and (transferadj start\$6)		1 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2
9	14	((((((dma and ram and (address adj	USPAT	2003/12/09
		count\$5) and interrupts) and		17:54
		register\$1) and (start\$6 adj address)) and predetermined) and temporary) and		
		(signal adj process\$7)and arithmetic)	1	
		and logic) and (transfer adj start\$6)	1	
10	0	((((((dma and ram and (address ad)	USPAT	2003/12/09
10	U	count\$5) and interrupts) and	USFAI	16:54
Ī		register\$1) and (start\$6 adj address))		10.54
,		and predetermined) and temporary) and		
		(signal adj process\$7) and arithmetic)	İ	
		and logic) and reprtition	j	
11	36	((((((dma and ram and (address adj	USPAT	2003/12/09
	30	count\$5) and interrupts) and	051111	16:54
		register\$1) and (start\$6 adj address))		13.3.
		and predetermined) and temporary) and		
		(signal adj process\$7) and arithmetic)		
		and logic) and repetition	1	
12	440	(dma and ram and (address adj count\$5))	USPAT	2003/12/09
		and (interrupt\$1 and register\$1 and		17:02
		(start\$6 adj address) and (arithmetic		1
		adj\$4 logic adj\$4 control\$7))		
13	147	((dma and ram and (address adj	USPAT	2003/12/09
		count\$5)) and (interrupt\$1 and		17:38
		register\$1 and (start\$6 adj address)		
		and (arithmetic adj\$4 logic adj\$4		
		control\$7))) and 710/\$.ccls.		
14	16	(((dma and ram and (address adj	USPAT	2003/12/09
		count\$5)) and (interrupt\$1 and		17:38
		register\$1 and (start\$6 adj address)		
		and (arithmetic adj\$4 logic adj\$4		
		control\$7))) and 710/\$.ccls.) and (dma		
7.5		adj interrupt\$1)		0.000 40 - 4
15	10	("4067059" "4245305" "4400772"	USPAT	2003/12/09
		"4403282" "4455620" "4481572"		17:49
		"4481578" "4514808" "4543629"		
16	_	"4558412").PN.		0000 (0.5 (5.5
16	5	((((((dma and ram and (address adj	USPAT	2003/12/09
		count\$5) and interrupts) and		17:55
		register\$1) and (start\$6 adj address))		
		and predetermined) and temporary) and		
		(signal adj process\$7) and arithmetic)		
		and logic) and ((interrupt\$7) adj		
		(conter\$1 or queue\$1 or stor\$5))		
17	14	("4103328" "4246637" "4268906"	USPAT	2003/12/09
		"4271468" "4296466" "4336588"		18:16
		"4514823" "4587609" "4591973"	1	
		"4602327" "4682304" "4688166" "4901232" "5031091").PN.		



L	Hits	Search Text	DB	Time stamp
Number				
_	562	dma and ram and (address adj count\$5)	USPAT	2003/12/08
		and interrupts		19:55
-	555	(dma and ram and (address adj count\$5)	USPAT	2003/12/08
		and interrupts) and register\$1		19:56
-	358	((dma and ram and (address adj count\$5)	USPAT	2003/12/08
		and interrupts) and register\$1) and		19:57
		(start\$6 adj address)		
-	288	(((dma and ram and (address adj	USPAT	2003/12/08
		count\$5) and interrupts) and		19:57
		register\$1) and (start\$6 adj address))		
		and predetermined		
-	171	(((dma and ram and (address adj	USPAT	2003/12/08
1		count\$5) and interrupts) and		20:16
		register\$1) and (start\$6 adj address))		
		and predetermined) and temporary		
-	8	("Re34850" "5001624" "5005121"	USPAT	2003/12/08
		"5237667" "5499348" "5504916"		20:10
		"5535417" "5634076").PN.		
-	23	((((dma and ram and (address adj	USPAT	2003/12/08
		count\$5) and interrupts) and		20:18
		register\$1) and (start\$6 adj address))		
		and predetermined) and temporary) and		
		dmac	İ	
-	98	((((dma and ram and (address adj	USPAT	2003/12/09
	•	count\$5) and interrupts) and		16:46
		register\$1) and (start\$6 adj address))		
		and predetermined) and temporary) and		
		(signal adj process\$7)		